

### MAIN CHARACTERISTICS

$I_D$	13A
$V_{DSS}$	450V
$R_{DS(on)-typ}$ (@VGS=10V Tc=25°C)	0.39Ω

### FEATURES

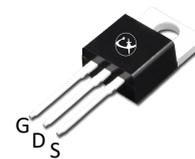
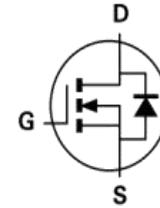
- Proprietary New Planar Technology
- Low Gate Charge Minimize Switching Loss

### APPLICATIONS

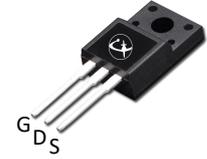
- Ballast and Lighting
- DC-AC Inverter
- Other Applications

### MECHANICAL DATA

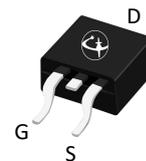
- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum, 10s per JESD 22-B106



TO-220AB



TO-220F



TO-263



TO-252



TO-251

### Product specification classification

Part Number	Package	Mode Name	Pack
CS13N45A1	TO-220AB	CS13N45A	Tube
CS13N45A2	TO-220F (0.5mm)	CS13N45A	Tube
CS13N45A3	TO-263	CS13N45A	Tube
CS13N45A3-R	TO-263	CS13N45A	Tape
CS13N45A4	TO-251	CS13N45A	Tube
CS13N45A5-R	TO-252	CS13N45A	Tape

### Maximum Ratings at Tc=25°C unless otherwise specified

Parameter	Symbol	Value			Unit
		220AB/263	220F	251/252	
Drain-Source Voltage	$V_{DS}$	450			V
Gate-Source Voltage	$V_{GS}$	±30			V
Continue Drain Current	$I_D$	13			A
Pulsed Drain Current (Note1)	$I_{DM}$	52			A
Power Dissipation	$P_D$	140	45	140	W
Single Pulse Avalanche Energy (Note1)	$E_{AS}$	538			mJ
Operating Temperature Range	$T_J$	150			°C
Storage Temperature Range	$T_{STG}$	-55 to +150			°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.89	2.7	0.89	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62	100	62	°C/W

Note1:Pulse test: 300 μs pulse width, 2 % duty cycle

### Electrical Characteristics at Tc=25°C unless otherwise specified

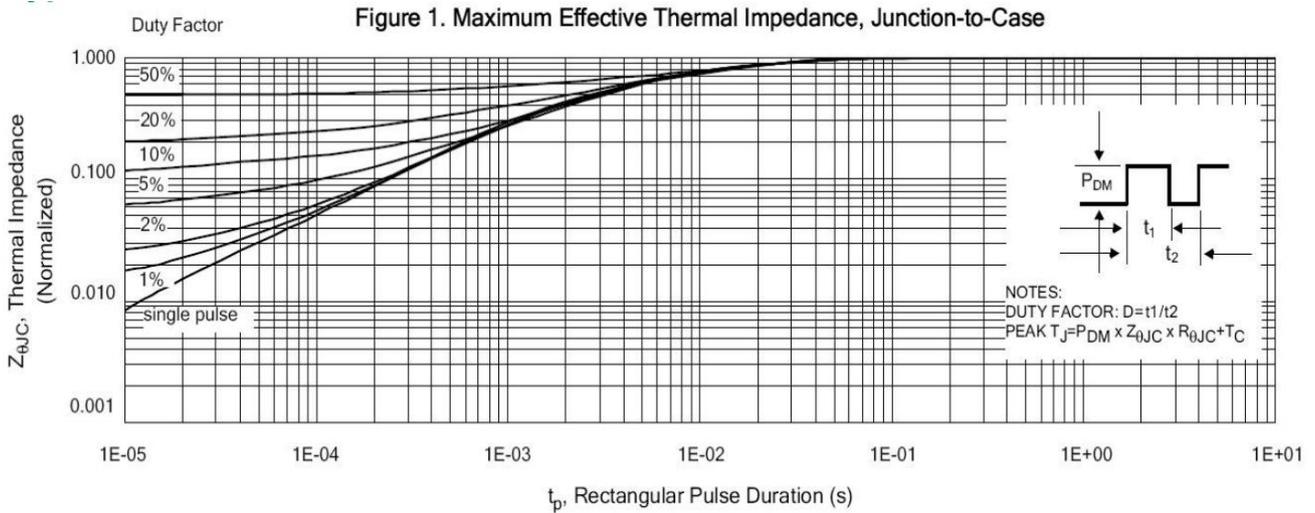
Parameter	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = 250 \mu A$	$BV_{DSS}$	450	-	-	V
Drain-Source Leakage Current	$V_{DS} = 450 V, V_{GS} = 0 V$	$I_{DSS}$	-	-	1	μA
Gate Leakage Current	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	$I_{GSS}$	-	-	±100	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	$V_{GS(th)}$	2	-	4	V
Drain-Source On-State Resistance	$V_{GS} = 10 V, I_D = 5.5 A$	$R_{DS(on)}$	-	0.39	0.5	Ω
Forward Transconductance	$V_{DS} = 15 V, I_D = 10 A$	$g_{fs}$	-	12	-	S
Input Capacitance	$V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz$	$C_{iss}$	-	1410	-	pF
Output Capacitance		$C_{oss}$	-	7.3	-	pF
Reverse Transfer Capacitance		$C_{rss}$	-	130	-	pF
Turn-on Delay Time(Note2)	$I_D = 11 A, V_{DD} = 200 V, R_G = 12 \Omega$	$t_{d(ON)}$	-	18	-	ns
Rise Time(Note2)		$t_r$	-	12	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	35	-	ns
Fall Time(Note2)		$t_f$	-	15	-	ns
Total Gate Charge(Note2)		$Q_G$	-	25	-	nC
Gate to Source Charge(Note2)	$I_D = 11 A, V_{DD} = 200 V, V_{GS} = 10 V$	$Q_{GS}$	-	6.7	-	nC
Gate to Drain Charge(Note2)		$Q_{GD}$	-	7.9	-	nC

### Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified

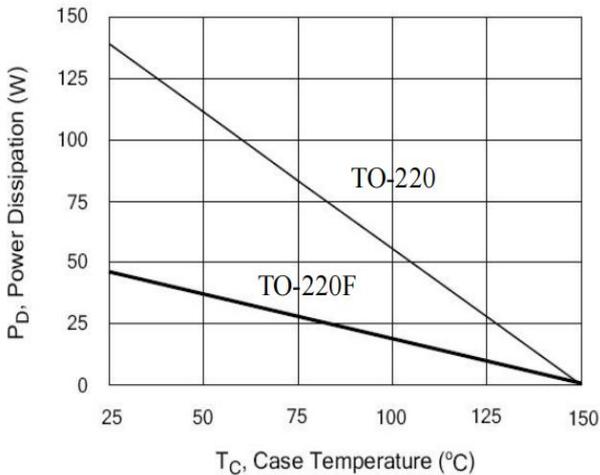
Parameter	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current	$T_J = 25^\circ C$	$I_S$	-	-	13	A
Maximun Body-Diode Pulsed Current(Note2)		$I_{SM}$	-	-	52	A
Drain-Source Diode Forward Voltage	$I_{SD} = 10 A$	$V_{SD}$	-	-	1.4	V
Reverse Recovery Time(Note2)	$I_{SD} = 10 A, V_{GS} = 0 V, dl_F / dt = 100 A/\mu s$	$t_{rr}$	-	334	-	ns
Reverse Recovery Charge(Note2)		$Q_{rr}$	-	2.1	-	μC

Note2:Pulse test: 300 μs pulse width, 2 % duty cycle

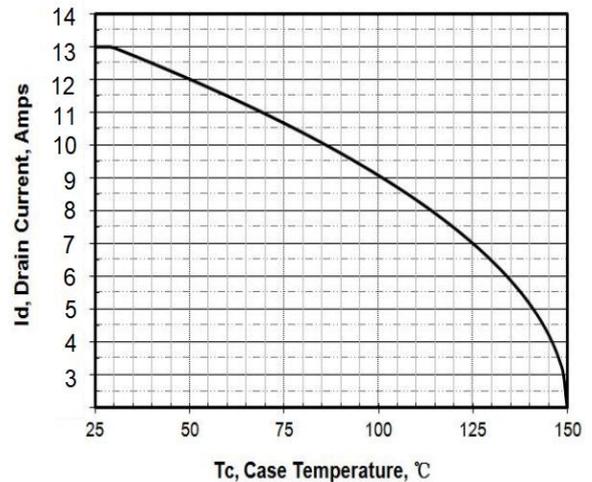
### RATINGS AND CHARACTERISTIC CURVES



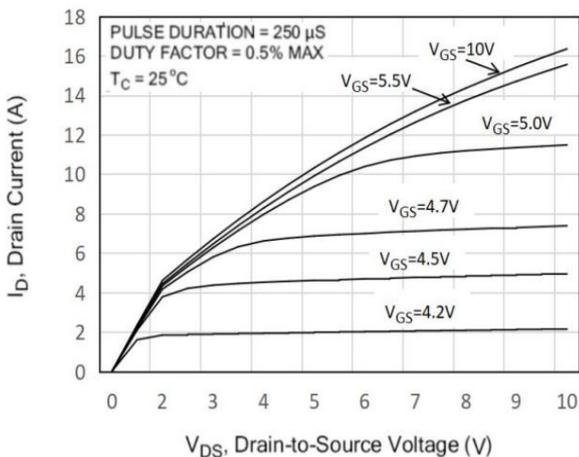
**Figure 2. Maximum Power Dissipation vs Case Temperature**



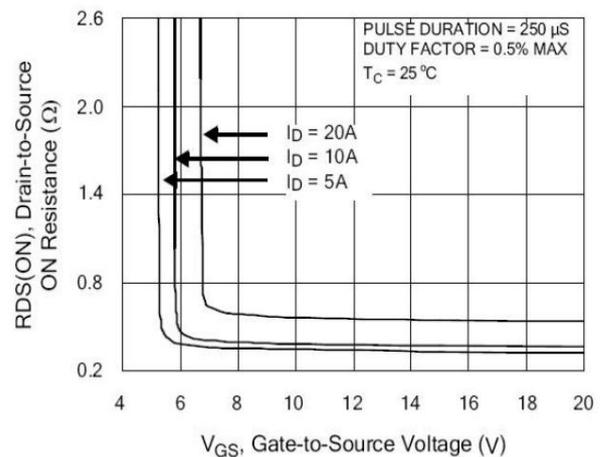
**Figure 3. Maximum Continuous Drain Current vs  $T_c$**



**Figure 4. Typical Output Characteristics**



**Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current**



### RATINGS AND CHARACTERISTIC CURVES

Figure 6. Peak Current Capability

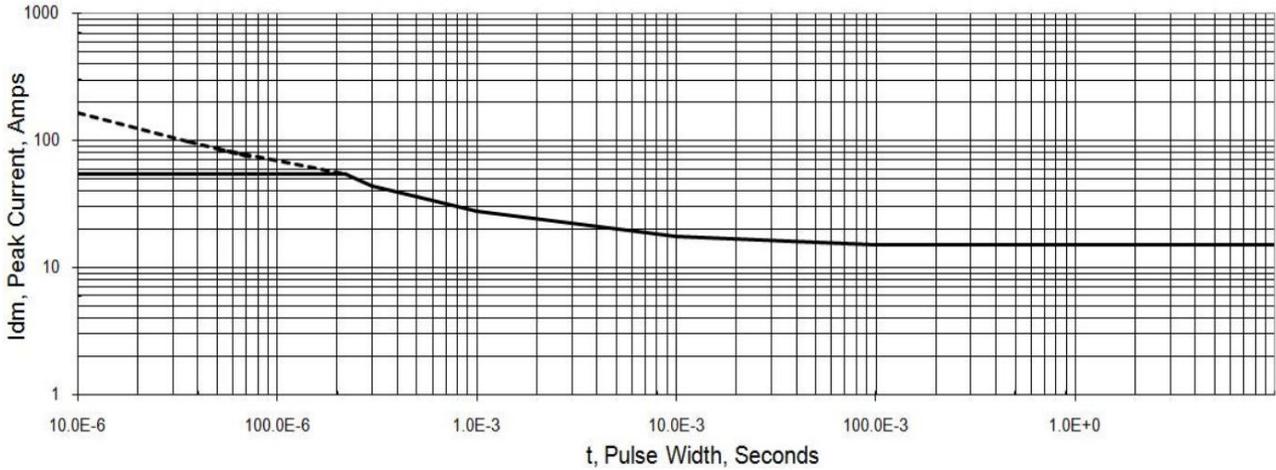


Figure 7. Typical Transfer Characteristics

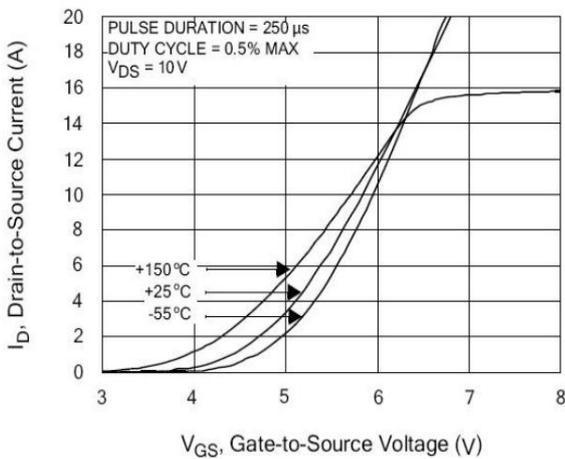


Figure 8. Unclamped Inductive Switching Capability

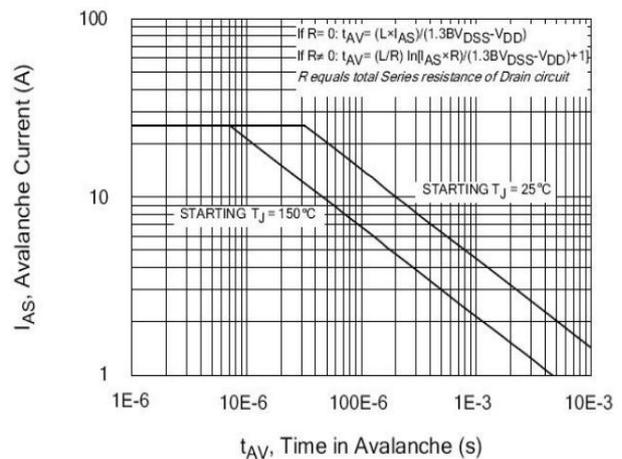


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

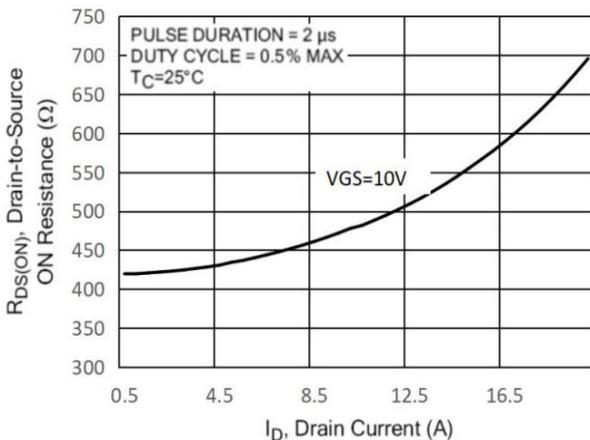
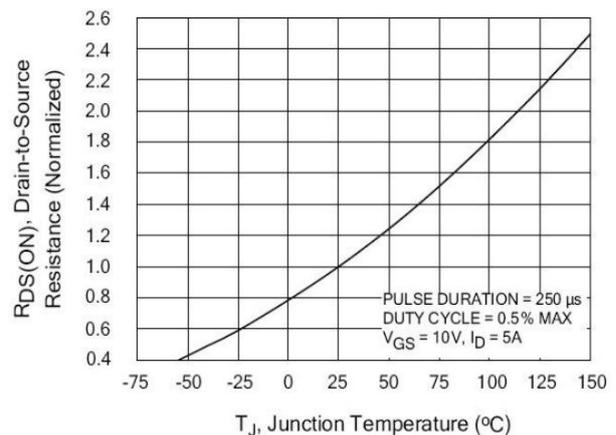


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



### RATINGS AND CHARACTERISTIC CURVES

Figure 11. Typical Breakdown Voltage vs Junction Temperature

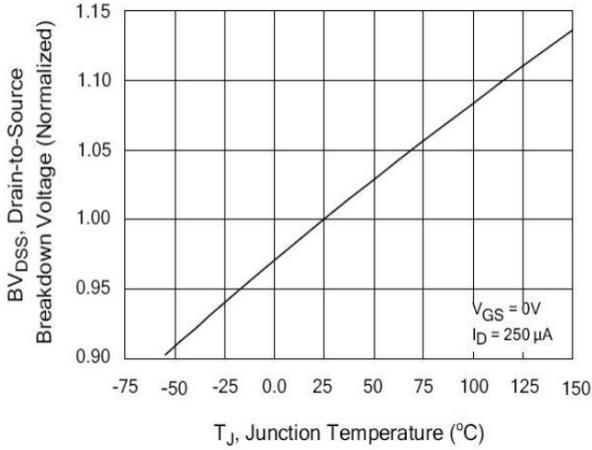


Figure 12. Typical Threshold Voltage vs Junction Temperature

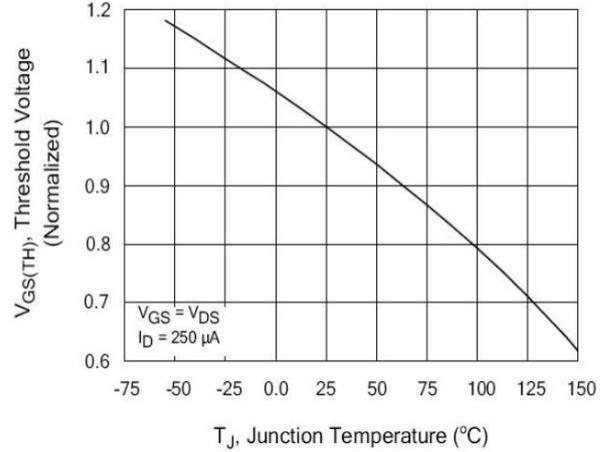


Figure 13. Maximum Safe Operating Area

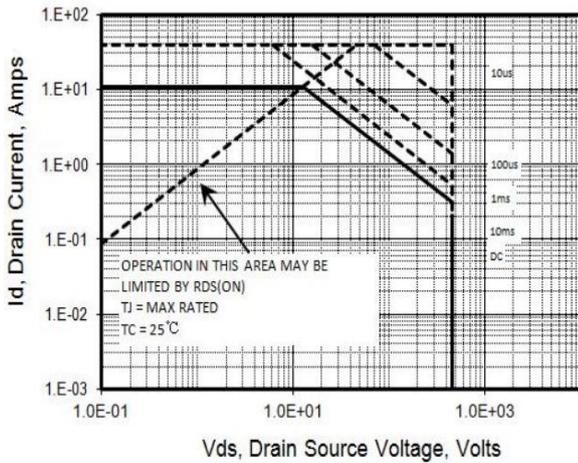


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

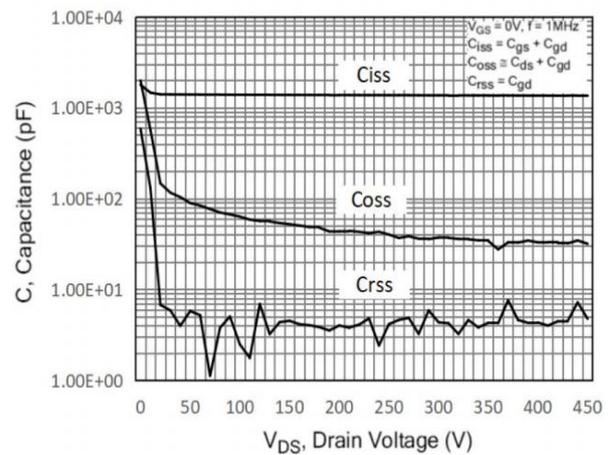


Figure 15. Typical Gate Charge vs Gate-to-Source Voltage

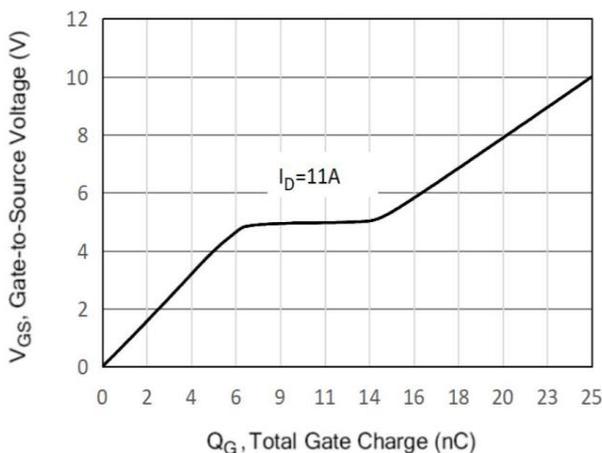
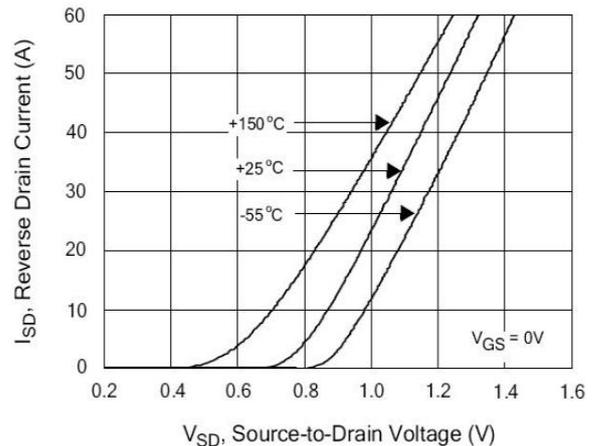
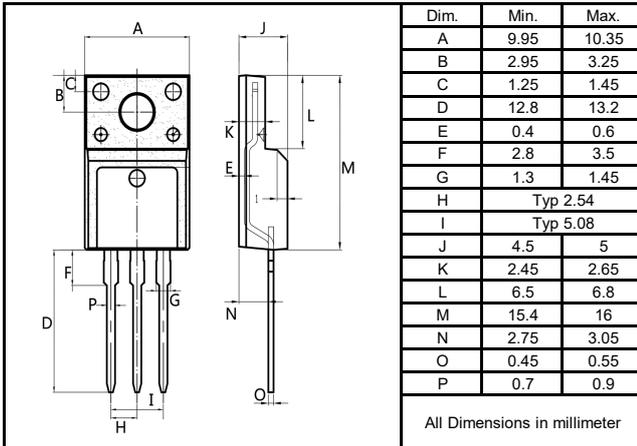
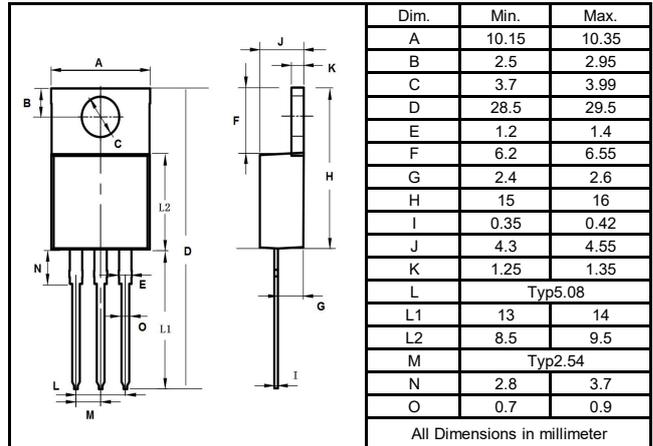
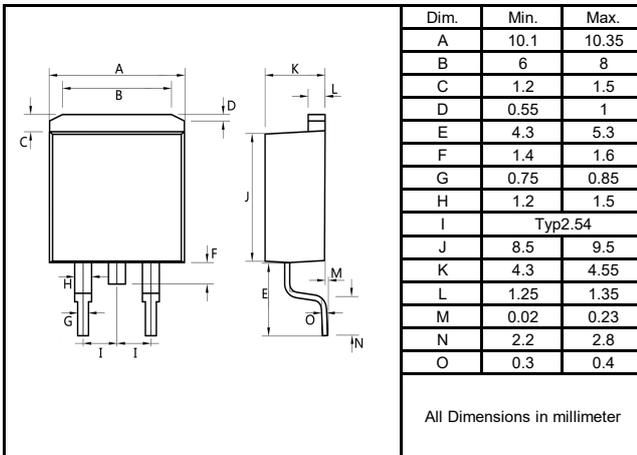
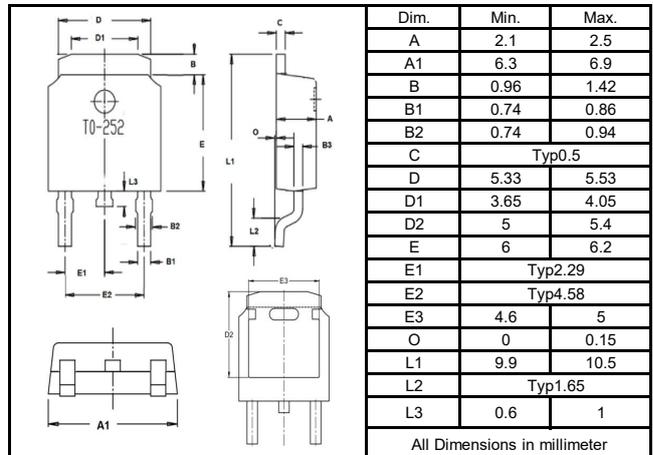
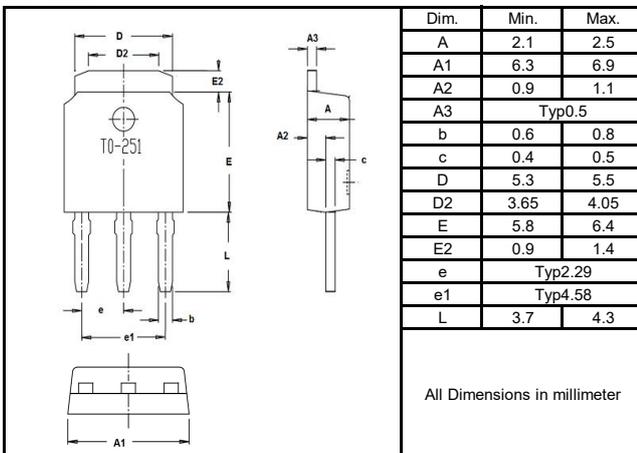


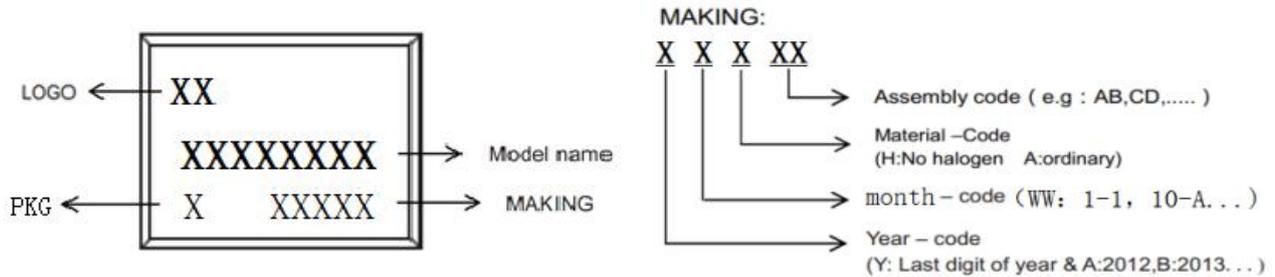
Figure 16. Typical Body Diode Transfer Characteristics



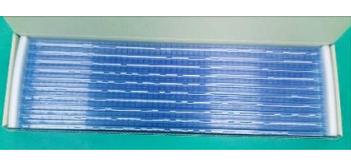
### Package Outline Dimensions millimeters

**T0-220F**

**T0-220AB**

**T0-263**

**T0-252**

**T0-251**


### Marking on the body



### packing instruction

PKG	Minimal Package	Mini Box	Box
TO-220F TO-220AB TO-263			
	50pcs/pdpe	1000pcs/box	5000pcs/box
TO-252			
	2500pcs/disk	5000pcs/disk	25000pcs/box
TO-263-R			
	800pcs/disk	1600pcs/disk	8000pcs/box
TO-251			
	80pcs/pdpe	4000pcs/box	24000pcs/box



# CS13N45A

## N-Channel Enhancement Mode Power MOSFET

### Notice

All product, product specifications and data are subject to change without notice to improve. The right to explain is owned by LINGXUN electronics company.

Confirm that operation temperature is within the specified range described in the product specification. Avoid applying power exceeding normal rated

power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

LINGXUN electronics shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

<http://www.lxmicro.com>

### Revision History

Rev	Changes	Date
1.0	First version	2025-7-15